

WHAT IS CLAIMED IS:

1. A method for generating sampling signals for use in an active matrix display, said method comprising steps of:

sequentially generating a plurality of pulse signals, every two adjacent pulse signals having a phase difference therebetween;

generating a guarding signal having alternate first level and second level; and

outputting sampling signals associated with said pulse signals in response to said guarding signal being at said first level, and exempting from outputting any sampling signal when said guarding signal is at said second level.

2. The method according to claim 1 wherein said plurality of pulse signals are generated in response to an enabling pulse signal and a pair of complementary clock signals.

3. The method according to claim 2 wherein rising edges and falling edges of said pair of complementary clock signals are consistent with said guarding clock signal being at said second level.

4. The method according to claim 1 wherein said sampling signals are produced according to a logic operation on each of said plurality of pulse signals with said guarding signal.

5. The method according to claim 4 wherein said logic operation is a NAND operation, and said first and second levels of said guarding signal are high and low, respectively.

6. The method according to claim 1 further comprising a step of adjusting levels of said sampling signals to control respective data switches for said active matrix display.

7. A method for generating sampling signals for use in an active matrix display, said method comprising steps of:

sequentially generating a plurality of pulse signals in response to an enabling pulse signal and a pair of complementary clock signals;

generating a guarding signal in response to said pair of complementary clock signals;

performing logic operations on said guarding signal and said plurality of pulse signals to obtain respective logic values; and

outputting sampling signals according to said logic values.

8. The method according to claim 7 wherein said guarding signal is logically low around rising edges and falling edges of said pair of complementary clock signals.

9. The method according to claim 8 wherein said logic operations are NAND operations.

10. The method according to claim 7 further comprising a step of adjusting levels of said sampling signals to control respective data switches for said active matrix display.

11. A device for generating sampling signals for use in an active matrix display, said device comprising:

a pulse signal generator for sequentially generating a plurality of pulse signals;

a guarding signal generator for generating a guarding signal; and

a logic operation circuit electrically connected to said pulse signal generator and said guarding signal generator, receiving said plurality of pulse signals and said guarding signal, and performing a logic operation on each of said plurality of pulse signals with said guarding signal to realize a logic state, and outputting a sampling signal only when said logic state is logically high.

12. The device according to claim 11 wherein said pulse signal generator comprises a plurality of data shift registers.

13. The device according to claim 12 wherein said plurality of pulse signals are generated in response to an enabling pulse signal and a pair of complementary clock signals.

14. The device according to claim 13 wherein rising edges and falling edges of said pair of complementary clock signals are consistent with said guarding signal being logically low.

15. The device according to claim 11 further comprising a level adjusting circuit electrically connected to said logic operation circuit for adjusting a level of said sampling signal to control a corresponding data switch of said active matrix display.